

No. IITH/40/R/2022/RTI/MS, dt. 01.08.2022

**Furnishing of Information under RTI Act.**

- 1 Application No. & Date : IITHY/R/E/22/00073, dated.06.07.2022
- 2 Name of the Applicant :
- 3 Date of Receipt of Application : 00.07.2022

4	Information Sought	Information given and details of documents, if any, furnished.
	What was the last admitted candidates Gate score for MVLSI in the year 2019 for MTECH TA of all categories?	Lowest Gate score of admitted candidates in 2-year M-Tech Microelectronics & VLSI program in 2019 via COAP. GEN: 814 OBC: 764 SC: 603 ST: None EWS: None
If you are not satisfied with the information provided, you may file an appeal with the following authority within 30 days:		
The Appeal, if any, should be addressed to:		Cmde M Nambiar, Ph.D. (Retd) Registrar & First Appellate Authority, IIT Hyderabad, Kandi, Sangareddy-502284 E-mail: registrar@iith.ac.in ; Tele: 040 2301 6055

Sd/-

(एम बद्रिनाथ)

(M Badrinath)

Joint Registrar & CPIO

Email: jr.cpio@iith.ac.in

Tele: 040 2301 6058

To,